

卷之三

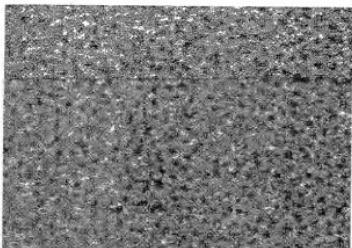


Fig. 1A

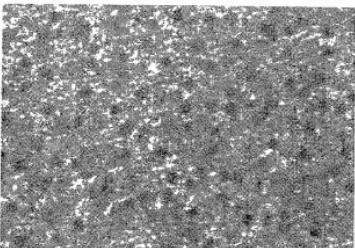


Fig. 1B

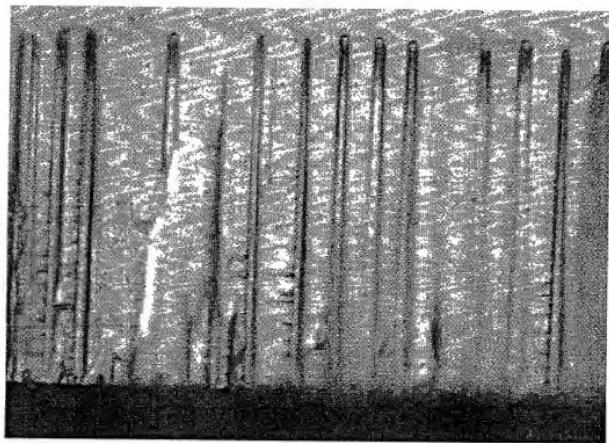


Fig. 2

00000000000000000000000000000000

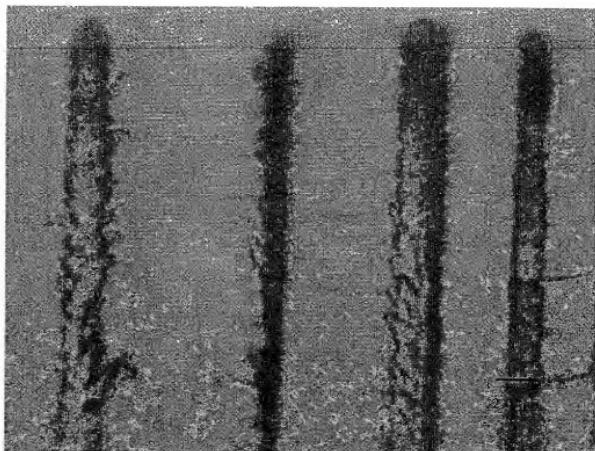


Fig. 3

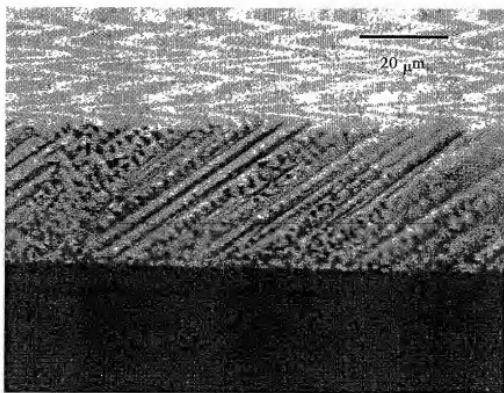


Fig. 4

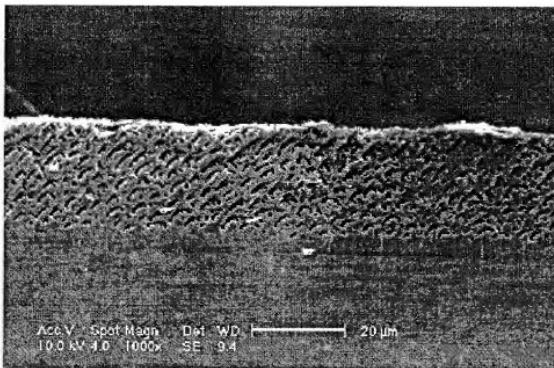


Fig. 5

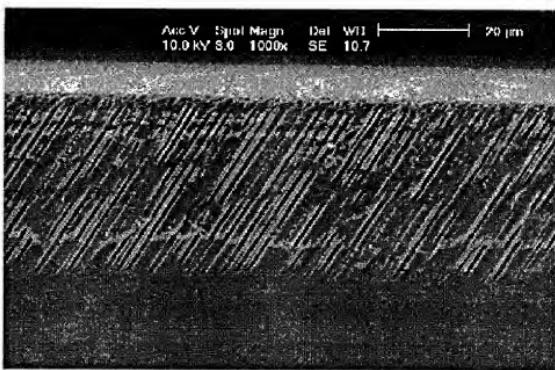


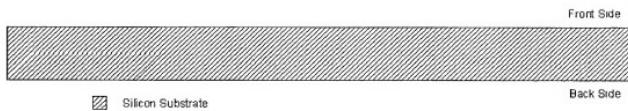
Fig. 6

010358-01 Neah Power 240x

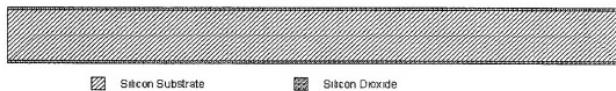
— 200  $\mu\text{m}$  —



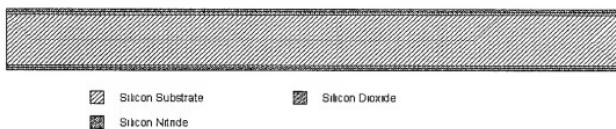
Fig. 7



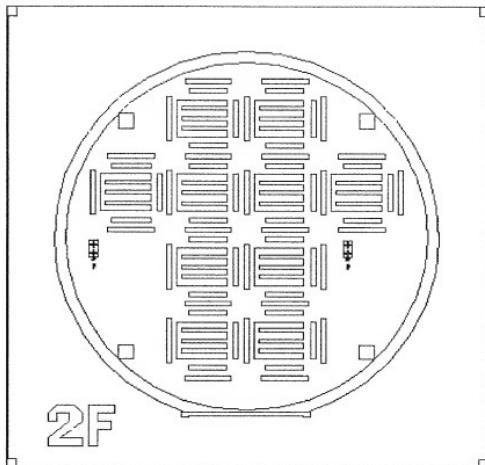
*Figure 8*



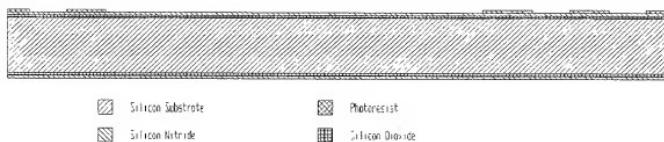
*Figure 9*



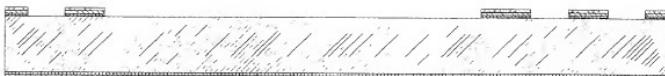
*Figure 10*



*Figure 11A*



*Figure 11B*



Silicon Substrate       Photoresist  
 Silicon Nitride       Silicon Dioxide

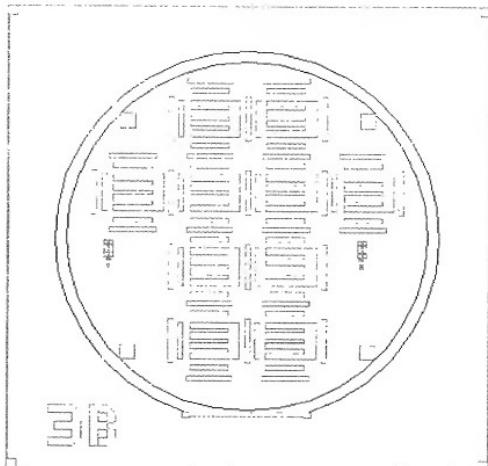
***Figure 12***



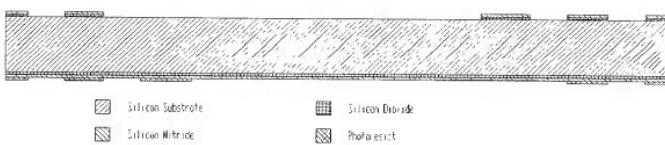
Silicon Substrate       Silicon Dioxide  
 Silicon Nitride     

***Figure 13***

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*Figure 14A*



*Figure 14B*



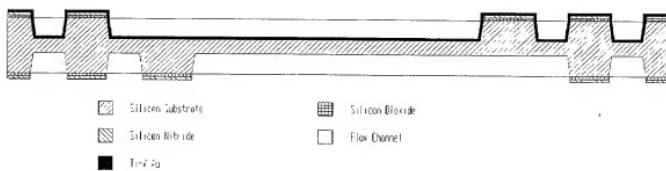
**Figure 15**



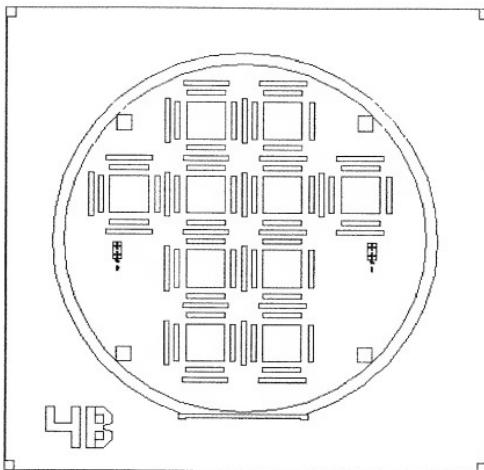
**Figure 16**



**Figure 17**



**Figure 18**



*Figure 19A*



■ Silicon Substrate	■ Silicon Dioxide
■ Silicon Nitride	□ Flow Channel
■ Ti-Au	▨ Photoresist

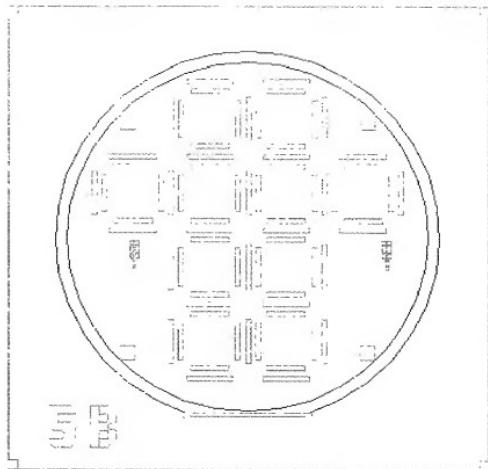
*Figure 19B*



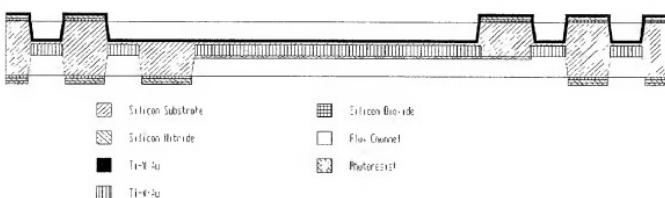
■ Silicon Substrate	■ Silicon Dioxide
■ Silicon Nitride	□ Flow Channel
■ Ti-Au	▨ Photoresist
■ Ti-N <sub>x</sub>	

*Figure 20*

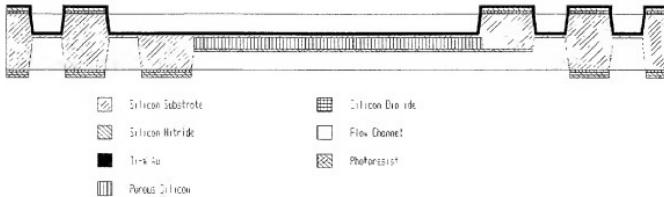
09929785 • 041001



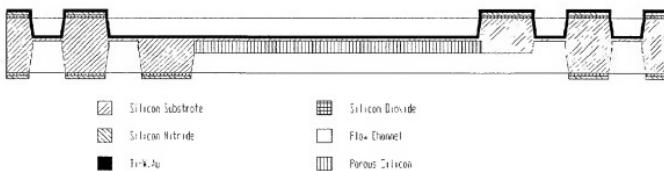
*Figure 21A*



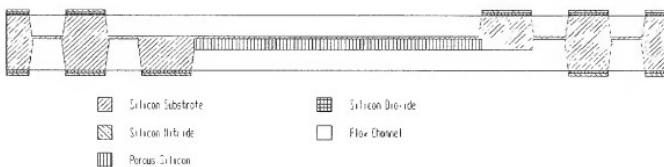
*Figure 21B*



***Figure 22***

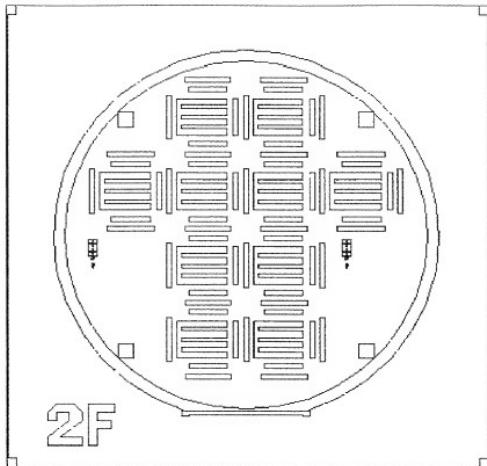


***Figure 23***

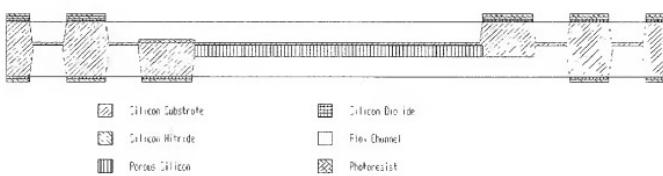


***Figure 24***

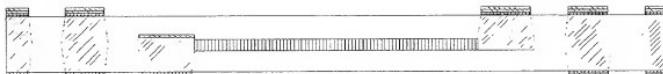
2067400-38762860



***Figure 25A***



***Figure 25B***



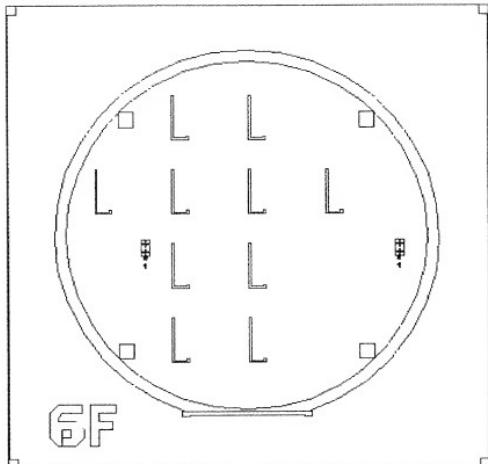
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<input checked="" type="checkbox"/> Silicon Nitride	<input type="checkbox"/> Flex Channel
<input checked="" type="checkbox"/> Porous Silicon	<input checked="" type="checkbox"/> Photoresist

***Figure 26***

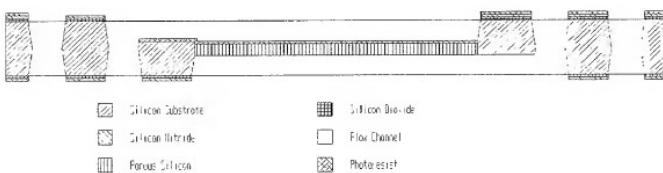


<input checked="" type="checkbox"/> Silicon Substrate	<input checked="" type="checkbox"/> Silicon Dioxide
<input checked="" type="checkbox"/> Silicon Nitride	<input type="checkbox"/> Flex Channel
<input checked="" type="checkbox"/> Porous Silicon	

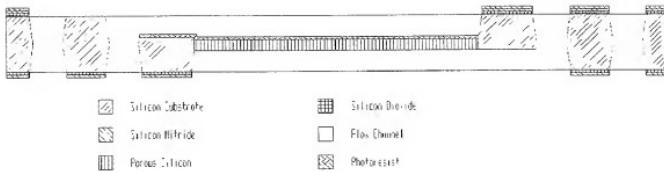
***Figure 27***



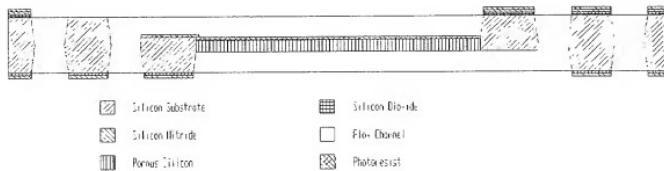
***Figure 28A***



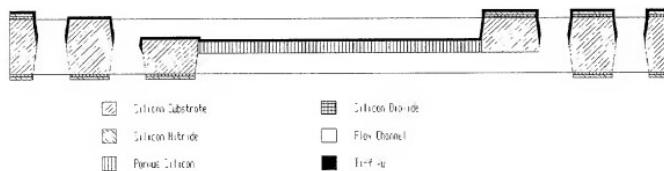
***Figure 28B***



**Figure 29**

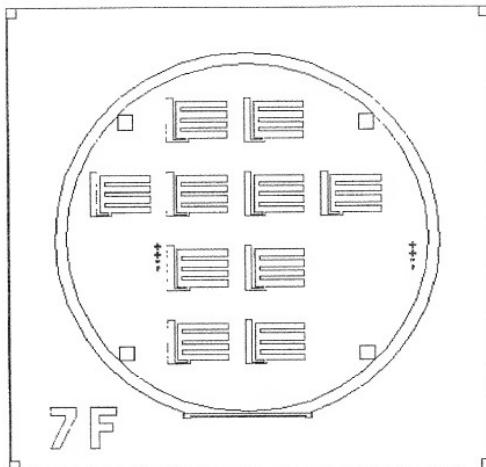


**Figure 30**

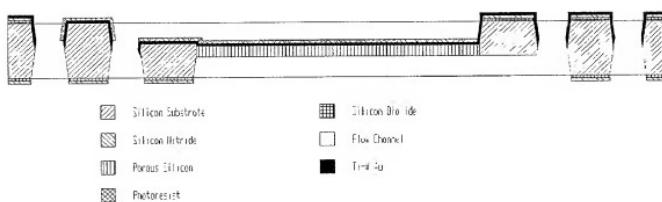


**Figure 31**

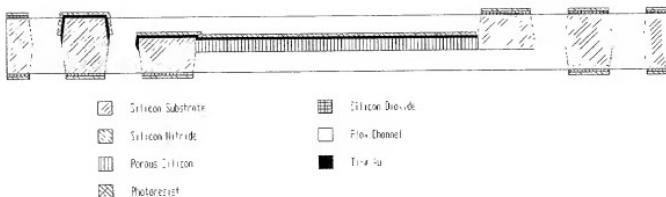
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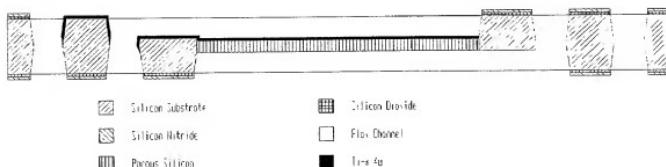
*Figure 32A*



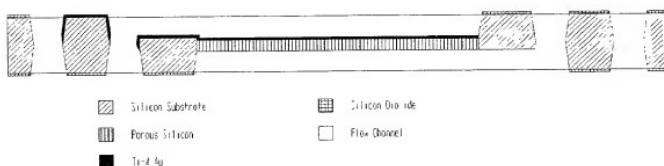
*Figure 32B*



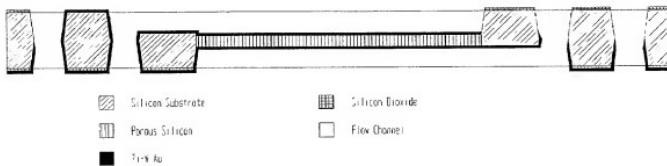
*Figure 33*



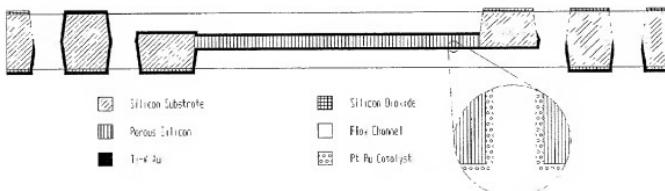
*Figure 34*



*Figure 35*

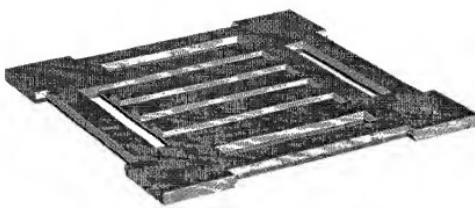


***Figure 36***

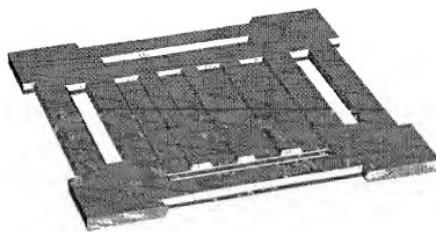


***Figure 37***

09597-001A050



*Figure 38A*



*Figure 38B*



□ Silicon Substrate

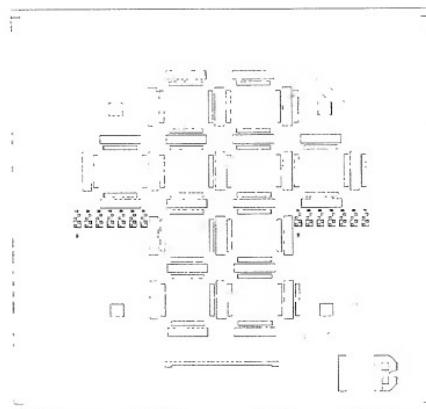
***Figure 39***



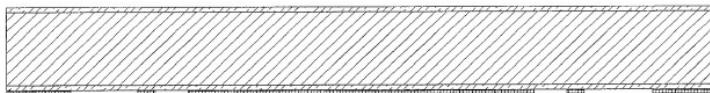
□ Silicon Substrate

▨ Silicon Nitride

***Figure 40***

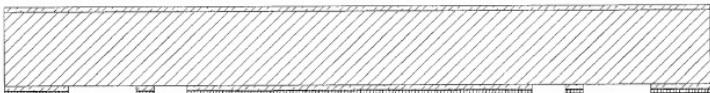


**Figure 41A**



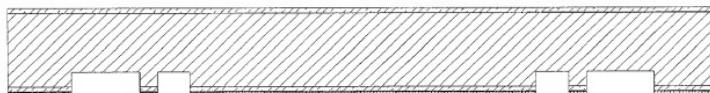
- Silicon Substrate
- ▨ Silicon Nitride
- ▨ Photo Resists

**Figure 41B**



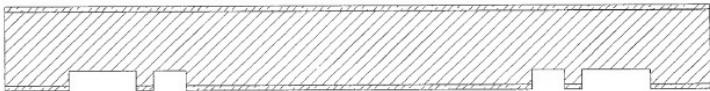
- Silicon Substrate
- ▨ Silicon Nitride
- ▨ Photo Resists

***Figure 42***



- Silicon Substrate
- ▨ Silicon Nitride
- ▨ Photo Resists

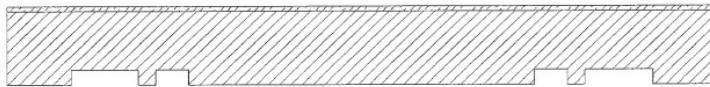
***Figure 43***



□ Silicon Substrate

▨ Silicon Nitride

***Figure 44***



□ Silicon Substrate

▨ Silicon Nitride

***Figure 45***

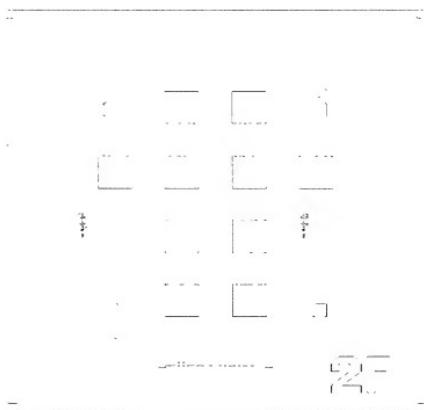


□ Silicon Substrate

▨ Silicon Nitride

■ Aluminum Contact

***Figure 46***



***Figure 47A***



- Silicon Substrate
- Photo Resists
- Silicon Nitride
- Aluminum Contact

***Figure 47B***



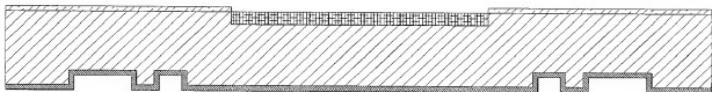
- Silicon Substrate
- Photo Resists
- Silicon Nitride
- Aluminum Contact

*Figure 48*



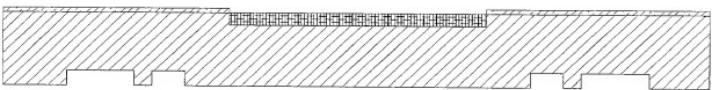
- [diagonal lines] Silicon Substrate
- [cross-hatch] Silicon Nitride
- [vertical lines] Aluminum Contact

*Figure* 49



Silicon Substrate  
 Silicon Nitride  
 Aluminum Contact

***Figure 50***



Silicon Substrate  
 Silicon Nitride  
 Porous Silicon

***Figure 51***



Silicon Substrate      Aluminum Contact  
 Silicon Nitride      Porous Silicon

***Figure 52***



**Figure 53A**



□ Silicon Substrate

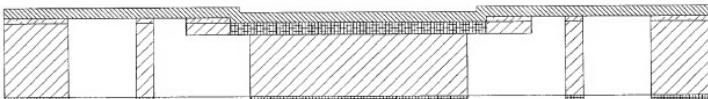
□ Silicon Nitride

□ Photo Resists

□ Aluminum Contact

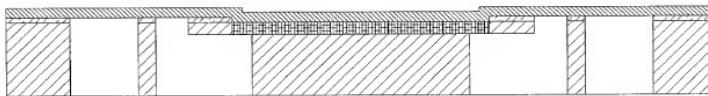
□ Porous Silicon

**Figure 53B**



□ Silicon Substrate      □ Aluminum Contact  
▨ Silicon Nitride      ▨ Porous Silicon  
▨ Photo Resists

***Figure 54***



□ Silicon Substrate      □ Aluminum Contact  
▨ Silicon Nitride      ▨ Porous Silicon

***Figure 55***



□ Silicon Substrate

■ Porous Silicon

▨ Silicon Nitride

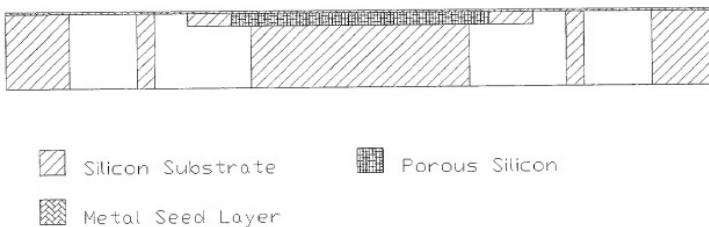
***Figure 56***



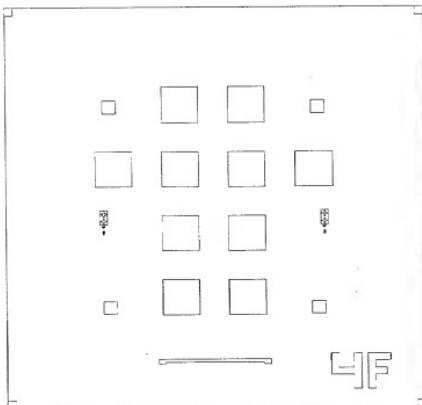
□ Silicon Substrate

■ Porous Silicon

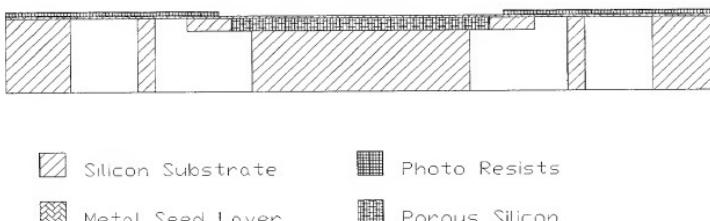
***Figure 57***



***Figure 58***



*Figure 59A*



*Figure 59B*



□ Silicon Substrate      □ Photo Resists  
▨ Pd Layer      ▨ Porous Silicon

***Figure 60***



□ Silicon Substrate      ▨ Porous Silicon  
▨ Pd Layer

***Figure 61***

***Figure 62A***



Silicon Substrate	Porous Silicon
Pd Layer	Photo Resists

***Figure 62B***



□ Silicon Substrate

■ Porous Silicon

▨ Pd Layer

▨ Photo Resists

***Figure 63***



□ Silicon Substrate

■ Porous Silicon

▨ Pd Layer

***Figure 64***

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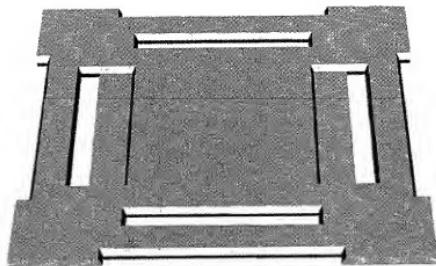


Fig. 65A

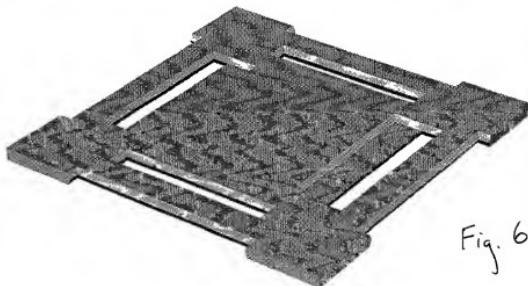


Fig. 65B

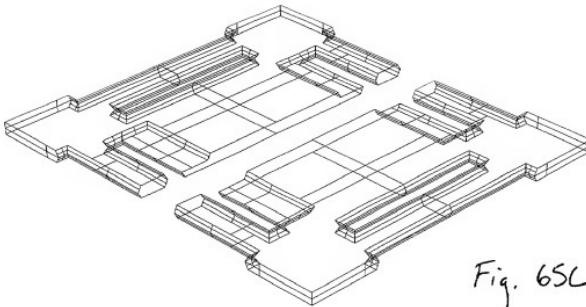


Fig. 65C